PQU1000 Series

1000W 5" x 8" AC-DC "U" Channel Power Supply Converter

VC FEATURES

- Compact high-density design with operation:
  - Up to 800W[4] natural convection.
  - 1000W with forced convection airflow at +50°C; no derating with input line voltage.
  -
- Voltage adjustment (-5%; +10%)[2] of Main V1 Output
- +5VAux/Standby (V2) and 12V (V3) Fan outputs
- 5" x 8" (127mm x 203.2mm) industry standard footprint; "U" channel form factor with industry "standard" mounting footprints.
  - 39.97mm maximum overall "U" Channel height.
  - 45.60mm nominal overall height with cover.
  - 67.93mm nominal overall height with integral fan cover.
- High efficiency of 95% typical at 50% load.
- True zero load operation of the Main (V1) output; no minimum load requirements[3].
- Remote sense for the main output.
- Universal AC input; active PFC; EN61000-3-2 Class A
- MTBF 1135Khrs; Telcordia SR332 Issue 3; M1 Case 3
- RoHS compliant.
- Active inrush protection.
- Active Current Share.
- IEC60601 Ed.3 medical (2 x MOPP Pri-Sec)
- Applied Part BF rating (isolation and patient leakage currents).
- 1 x MOPP Pri-Chassis Ground)
- IEC62368-1
- Designed to comply with IEC60601-2 4th Edition EMC Standard Requirements[1]

[1] End User Systems verification shall be required.
[2] 54V output adjustment range is ±5% max to maintain max voltage to <60V to maintain SELV limits.
[3] Zero load output voltage may exceed the regulation window.
[4] See derating curves.

DESCRIPTION

The PQU1000 series products are rated at 1000W operating from a wide range AC input.

Offered as a 5" x 8" industry standard format capable of providing a continuous 1000W[1] output with forced convection, and up to an impressive 800W[3], at +50°C with natural convection airflow, the product can accommodate a cover with, or without, integral top mounted fan assembly.

All variants are provided with constant current overload protection, allowing operation with motors, solenoids and high capacitance loads.

Active current sharing is provided that allow connection of PQU1000 power modules, in either non redundant parallel, or parallel redundant deployments, whilst maintaining equal current share between modules.

Provision of an adjustable main output, standby/auxiliary and fan outputs, plus PMBus™ Power Management Bus, will enable this technically superior solution to be deployed across multiple market sectors, complemented by safety certification applicable to medical, audio, video, communication and ITE standards.

ORDERING GUIDE (MODEL NUMBER)

Model (Order) Number	Main output (V1)		Aux Output (V2)		Fan Output (V3)[4]	
	Voltage Vdc	Current Adc; @ 50°C; 1000W[1]	Vdc	Current Adc @ 50°C	Vdc	Current Adc @ 50°C
PQU1000-12	12	83.3	5	1.0	12	1.0
PQU1000-24	24	41.7				
PQU1000-48[2]	48	20.8				
PQU1000-54[2]	54	18.5				
PQU1000-COVER[3]	Optional cover kits; End User assembly required.					
PQU1000-FT-COVER[5]						

[1] Require external system airflow or PQU1000-FT-COVER with integral fan.

[4] Only available for forced air cooled deployments (not rated for convection cooled

[2] PoE Isolation Compliant.
[3] Some minor derating required.
(deployments).
[5] Cover assembly with integral top mounted fan.

INPUT CHARACTERISTICS					
Parameter	Conditions	Min	Nom	Max	Units
Input Voltage AC Operating Range	Single Phase	90[1]	100/240	264	V_{AC}
Input Frequency		47	50/60	63	Hz
Turn-on input voltage	Input rising	75[1]		90	Vac
Turn-off input voltage	Input falling	65		80	Vac
Maximum input current (target)	Vin = 90Vac; Full Load (1000W FL)			13	Arms
Inrush Current	230VAC,Cold start, 25°C;			50	Apk
Power Factor	At 115VAC/230VAC, full load	0.95			W/VA
Hold-up Time (Target)	90VAC; Full Load	10			ms
Target Efficiency @ 230Vac	20% Full Load		92		%
	50% Full Load		95		
	100% Full Load		95		

[1]Operation at 80Vac is possible at 650W and +50°C; however, the specification is not guaranteed at an input voltage of less than 90VAC.

MAIN OUTPUT CHARACTERISTICS (ALL MODELS)					
Parameter	Conditions	Min	Nom	Max	Units
Line, Load Regulation	Main (V1) Output[1]			±5	%
Minimum Load Capability	Stable Operation	0			A
Output Ripple	Zero to Full Load[2]			1%	mV_{PP}
Transient Response[3]	50% load step, 1A/µsec slew rate and min 10% load i.e. 10% to 60%; 100% to 50%			± 5	%
Settling Time to 1% of Nominal				2	msec
Turn On Delay	After application of input power			3	sec
Output Voltage Rise			200		msec
Remote Sense[4]	Compensates for up to 500mV of total lead drop (output and return connections) with remote sense connected. Protected against short circuit and reverse connection.			1	%

[1] Zero load output voltage may exceed the regulation window however will not cause OVP to engage or PWOK to change to low state.
[2] Ripple and noise are measured with 0.1uF ceramic capacitor and 10uF tantalum capacitor. A short coaxial cable with 50 ohm termination is used. Min 120uF cap required at the output to keep ripple within 1% for 54V output. Min 10% load current required, to maintain ripple within 1% for the 12V output model.
1A min load for all other models.
[3] Minimum of 1 second time between consecutive transients; requires 10% minimum load.
[4] If remote sense is left unterminated (floating) then the output voltage set point will increase by 500mVdc.

AUXILIARY OUTPUT CHARACTERISTICS					
Auxiliary Output	Aux Output Voltage	Load Current	Load Capacitance	Line, Load, Cross Regulation	Ripple Voltage & Noise
Aux (V2)	5V	0 to 0.5A	0 to 220µF	4.75 to 5.25Vdc	$100mV_{PP}$

FAN OUTPUT CHARACTERISTICS (ALL MODELS)					
Auxiliary Output[1,2]	Aux Output Voltage	Load Current	Load Capacitance	Line, Load, Cross Regulation	Ripple Voltage & Noise
Aux (V3)	12V	0 to 0.6A	0 to 220µF	10.8 to 13.2Vdc	$120mV_{PP}$

[1] Not recommended for "general use" due to its semi regulated characteristic. The output is for use with a fan intended to cool the PQU650M; therefore, if the PQU650M is convection cooled only then this output should not be used.
A 1.5A non-replaceable fuse is provided in this output for overload protection.
[2] Only available for forced convection cooled deployments (not available for natural convection cooled deployments).

MAIN OUTPUT CHARACTERISTICS (ALL MODELS EXCEPT UNLESS NOTED)				
Parameter	Conditions	Typ.	Max.	Units
Transient Response[1]	50% load step, 1A/µsec slew rate and min 10% load i.e. 10% to 60%; 100% to 50%		± 5	%
Settling Time to 1% of Nominal			2	msec
Turn On Delay	After application of input power		3	sec
Output Voltage Rise		200		msec
Remote Sense[2]	Compensates for up to 500mV of total lead drop (output and return connections) with remote sense connected. Protected against short circuit and reverse connection.		1	%

[1] Minimum of 1 second time between consecutive transients; requires 10% minimum load.
[2] If remote sense is left unterminated (floating) then the output voltage set point will increase by 500mVdc.

ENVIRONMENTAL CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Storage Temperature Range		-40		85	°C
Operating Temperature Range[2]	See power derating curves	-30		70	
Operating Humidity	Non-condensing	10		95	%
Operating Altitude		0		5000[1]	m
MTBF	Telcordia SR-332 Issue 3; M1C3 @ 25°C Telcordia SR-332 Issue 3; M1C3 @ 40°C		2140K 1135K		Hours
Shock	30G, non-operating; Validation testing per IEC 60068-2-27, test Ea. 30G, 11msec half-sine, 3 shocks per face, 6 faces.	Complies			
Operational Vibration	Sine Sweep; 5-150Hz, 2G Random Vibration, 5-500Hz, 1.11G	Complies			
Safety – Medical Standards[1] 2 x MOPP (Primary-Secondary) Applied Part Type BF	IEC 60601-1:2005/AMD1:2012 [TÜV SÜD] CAN/CSA-C22.2 No. 60601-1:2014 [TÜV SÜD] ANSI/AAMI ES60601-1:2005/A1:2012-08 [TÜV SÜD] EN 60601-1:2006/A1:2013 [TÜV SÜD]				
Safety – ITE, Audio/Video/Communications & Consumer Standards[1]	IEC 62368-1:2014 [CSA] CAN/CSA-C22.2 No. 62368-1:14[CSA] UL 62368-1 2nd Ed. [CSA] GB 17625.1-2012; GB 4943.1-2011; GB/T 9254-2008 (Class A) [CCC] EN IEC 62368-1:2020/A11:2020 [TÜV SÜD] CE [Self-Declaration] UKCA [Self-Declaration]				
Fuses (Input)	Dual Fuses; Line and Neutral; 16A Fast Acting; 250V				
Outside Dimensions ("U" Channel only).	5.0" x 8.0" x 1.69" (127.0mm x 203.2mm x 40.0mm) nominal				
Weight (typ.)	1.18/2.60				kg/lbs.

[1]Meets 5000 M max. altitude for Medical certification requirements.
[2] Starts up at -40°C; however full specification guaranteed at -30°C.

ISOLATION CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation	Primary to Chassis	1500			Vac[2]
	Primary to Secondary (2xMOPP)	4800			
	Secondary to Chassis[1]	1500			
	Main Output to other outputs[1]	1500			
Touch Currents (IEC62368-1)	264Vac, 60Hz, 25°C			1.5	mApk
Patient Leakage Current (under normal conditions)	Meets relevant max Type B and BF patient leakage current limits			100	µA

[1] Meets PoE isolation limits
[2] Isolation is verified during safety compliance testing by the use of an equivalent DC voltage as defined by IEC60601-1 3rd Edition; Section 8.8.3 using values as per Table 6, based upon the relevant peak working voltage.

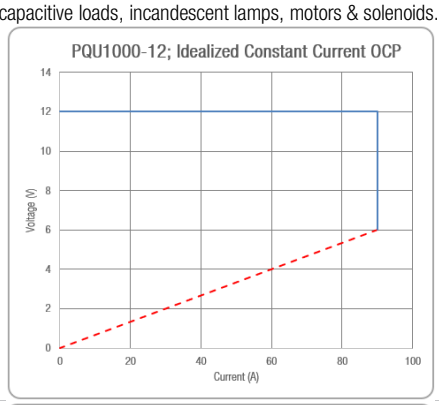
PROTECTION CHARACTERISTICS

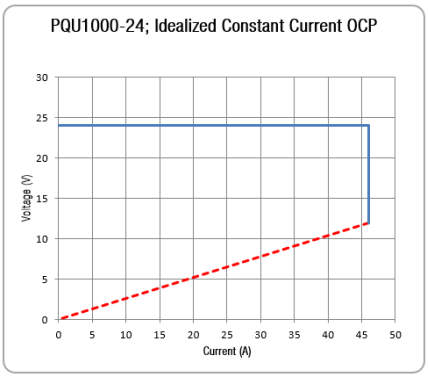
Parameter		Conditions	Min.	Typ.	Max.	Units
Over Voltage Protection		V1 (main output) latching	110		140	%Vdc[2]
		V1 (48V & 54V models) latching			60	Vdc
		V2 (aux output) latching	5.5		7.5	
Over Current Protection		V1, See Constant Current curves below				
		V2, cycling, auto-recovery	110		150	%
		V3; non-resettable fuse[1]			1.5	Adc
Over Temperature Protection	Primary Heatsink Temperature				130	°C
	Secondary Temperature				130	
Remote Sense Short Circuit Protection				Complies		--
Remote Sense Reverse Connection Protection				Complies		--

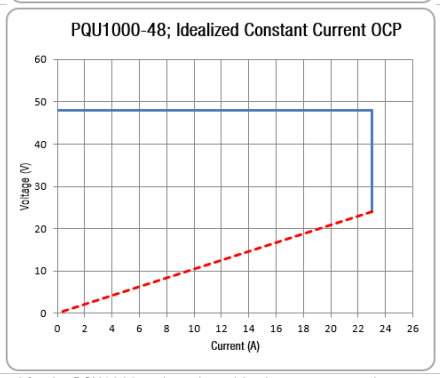
[1] OCP of the 12V Fan (V3) output is provided by a non-user replaceable SMD fuse rated at 1.5A; therefore if ruptured the 12V Fan output will not be available and the fuse shall require to be replaced.
[2] Refers to percentage of nominal voltage.

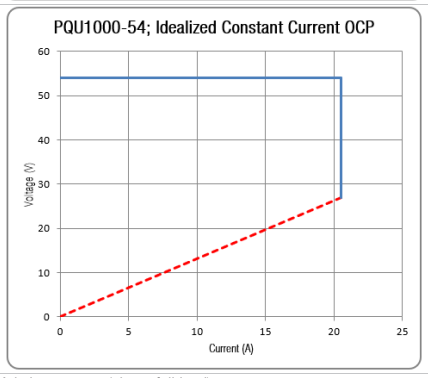
CONSTANT CURRENT CHARACTERISTICS

The idealized Constant Current characteristic are as shown in the following curves. This feature will enable the PQU1000 to successfully start in to application loads exhibiting large inrush current i.e. large capacitive loads, incandescent lamps, motors & solenoids.









1. Curves generated for the PQU1000 variants by subjecting output to an incremental (constant resistance load, equivalent to 1Adc increments (above full load).
2. The resultant curve shows the current limited to a constant "brick wall" shown by the blue portion of curve.
3. If the load current is further incremented the output will enter "hiccup" (recycling on/off) shown by the red dashed curve, commencing when the output voltage falls to ~50% of the nominal set point.
4. If the overload current is maintained above maximum load for an extended period the "hiccup" operation will continue indefinitely while the overload persists. In the event that the overload is maintained just below that where "hiccup" operation is initiated then, dependent on the prevailing operating conditions, the power module may enter thermal protection.

CURRENT SHARING

Model Number	Description
All PQU1000 Variants	Main output current share is achieved using active current sharing. The current sharing signal is connected between sharing units (forming an ISHARE signal bus). It is an input and/or an output (bi-directional analogue bus) as the voltage on the signal line controls the current share between sharing units. A power supply will respond to a change in this voltage but a power supply can also change the voltage depending on the load drawn from it. On a single unit the voltage on the pin (and the common ISHARE bus would read approximately 8Vdc at 100% load (module capability). For two identical voltage variants sharing the same 100% load this would read approximately 4Vdc for perfect current sharing (i.e. 50% module load capability per unit). Startup of parallel power supplies is not internally synchronized. No more than 1000W combined power is allowed at start-up. To account for±10% full load current sharing accuracy, the output power must be derated by 15% when units are operated in parallel. Current sharing can be achieved with or without remote sense connected to the common load. External ORing protection is recommended (see Application notes, ACAN-TBA for additional details). The +5V_STANDBY (Aux) V2 outputs cannot be can be tied together for increased power or redundancy; however, +5V_STANDBY_RTN can be tied together to create a common return for the signals between units that share the Main V1 output. It is not recommended that the 12V Fan (V3) outputs are connected in parallel since these outputs are only semi regulated, and only intended to supply an external fan (or that of the PQU1000-FT-COVER integral fan).

EMISSIONS AND IMMUNITY

Characteristic	Standard	Compliance
Input Current Harmonics	IEC/EN 61000-3-2	Class A
Voltage Fluctuation and Flicker	IEC/EN 61000-3-3	Complies
Conducted Emissions	CISPR 32/EN 55032	Class B
	FCC Part 15	Class B
Radiated Emissions	CISPR 32/EN 55032	Class A
	FCC 15.109 - 3 meter	Class A
ESD Immunity	IEC/EN 61000-4-2	Level 4, Criterion B
Radiated Field Immunity	IEC/EN 61000-4-3	Level 3, Criterion A
Electrical Fast Transient Immunity	IEC/EN 61000-4-4	Level 3, Criterion A
Surge Immunity	IEC/EN 61000-4-5	Level 3, Criterion A (Com. Mode: 2kV 12 ohm, Diff. Mode: 1kV, 2 ohm)
Radiated Field Conducted Immunity	IEC/EN 61000-4-6	Level 3, 10V/m, Criterion A
Magnetic Field Immunity	IEC/EN 61000-4-8	Level 3, Criterion A
Voltage dips, interruptions	IEC/EN 61000-4-11	Level 3, Criterion B

EMI CONSIDERATIONS

To comply with safety standards, the input connector must be properly grounded to protective earth (see mechanical dimension notes).
Pre-compliance testing has shown the stand-alone power supply to comply with EN55032 class A radiated emissions; testing was based on adding a toroid (4 turns of both main output wires wound as common mode choke on FAIR-RITE#5961002701).
Radiated emission results vary with system enclosure and cable routing paths.
A minimum 10% load current is required, on the main output.

STATUS AND CONTROL SIGNALS TABLE

Signal Name	I/O	Description	Interface Details
DC_OK_H J301 Pin 2	Output	The signal is asserted, driven high, by the power module to indicate that all outputs are valid. If the V1 (Main) and V2 (+5V_STANDBY) outputs fail, then this output will be driven low.	Pulled up internally to 10K to VDD[1] A logic high >2.0Vdc A logic low <0.8Vdc Driven low by internal CMOS buffer (open drain output).
PS_ON_H (V1 Main Output Enable/Disable) J301 Pin 4	Input	The PS_ON_H signal is intended to be unterminated (open circuit) or pulled up to V2 +5V_AUX the V1 Main output to "**turn on**" (enable) the Main V1 Output. To turn "off" (disable) the Main V1 output the PS_ON shall be pulled low (sink current >2mA) to +5V_AUX_RTN. This pin must be pulled low (sink current >2mA) to +5V_AUX_RTN to **turn off** the main output. The +5V_AUX output is independent of the PS_ON_H signal, and comes up automatically when the input AC source is applied within specified operating ranges. The +5V_AUX output is independent of the PS_ON_H signal, and comes up automatically when the input AC source is applied within specified operating ranges.	The PS_ON_H signal is intended to be unterminated (open circuit) or pulled up to V2 +5V_AUX. Sink current >2mA) to +5V_AUX_RTN to **turn off** the main output.
AC_OK_H J301 Pin 3	Output	The signal output is driven high when input source is available and within acceptable limits. The output is driven low to indicate loss of input power. There is a minimum of 1ms pre-warning time before the signal is driven low prior to the PWR_OK signal going low. The power supply must ensure that this interface signal provides accurate status when AC power is lost.	Pulled up internally via 10K to 3.3Vdc. A logic high >2.0Vdc A logic low <0.8Vdc Driven low by internal CMOS buffer (open drain output).
+VE SENSE; J702 Pin 2 -VE SENSE_Return; J702 Pin 4	Input	Sense connections are provided to compensate for the voltage drop in cables to the load. The voltage sense will interact with the internal module regulation loop to compensate for voltage drops due to connection resistance between the output connector and the load. Local sensing can be achieved in two ways: • If the ISHARE function is not required then jumper (headers) can be fitted between J702 Pins 1 & 2 and J702 Pins 4 & 5 (see Mechanical Outline section for additional details). • If ISHARE is required (i.e. load sharing between parallel connected modules) then jumper wires/cables can be fitted to the mating connector between J702 Pins 1 & 2 and J702 Pins 4 & 5. If (remote) sensing at the load is required then cables can be extended from the mating connector to the load: • +VE SENSE, J702 Pin 2 connected to +VE of the load • -VE SENSE_Return, J702 Pin 4, connected to -VE_MAIN_Return (J4 Pin4).	Compensation for up to 0.5Vdc total connection drop (output and return connection).
ISHARE J702 Pin 3	I/O	The current sharing signal is connected between sharing units (forming an ISHARE bus). It is an input and/or an output (bi-directional analogue bus) as the voltage on the line controls the current share between sharing units. A power supply will respond to a change in this voltage but a power supply can also change the voltage depending on the load drawn from it. On a single unit the voltage on the pin (and the common ISHARE bus would read approximately 8Vdc at 100% load (module capability). For two identical units sharing the same 100% load this would read approximately 4Vdc for perfect current sharing (i.e. 50% module load capability per unit).	Analogue voltage: Approximately +8Vdc maximum; 10K to VE_MAIN_Return

STATUS AND CONTROL SIGNALS TABLE

Signal Name	I/O	Description	Interface Details
ADDR J301 Pin 5	Input	An analogue input that is used to set the address of the internal slave devices (EEPROM and microprocessor) used for digital communications. Connection of a suitable resistor to +VSB_Return, in conjunction with an internal resistor divider chain, will configure the required address. The external resistor shall enable up to eight (8) separate addresses to be configured.	DC voltage between the limits of 0 and +3.3Vdc.
SMB_ALERT J301 Pin 6	Output	The signal output is driven low to indicate that the power supply has detected a warning or fault and is intended to alert the system. This output must be driven high when the power is operating correctly (within specified limits). The signal will revert to a high level when the warning/fault stimulus (that caused the alert) is removed. As reported by PMBusTM STATUS_X Registers, with exception of STATUS_CML.	Pulled up internally via 10K to VDD*. A logic high >2.0Vdc A logic low <0.8Vdc Driven low by internal CMOS buffer (open drain output).

HEX Address Combinations by Analogue Method; ADDR External Resistance Values

ADDR External Resistance to RTN/Ground (KΩ; ±5% Tolerance)	Power Module Secondary Main Controller (Serial Slave Address)*
0.82	0xB0
2.7	0xB2
5.6	0xB4
8.2	0xB6
15	0xB8
27	0xBA
56	0xBC
180	0xBE

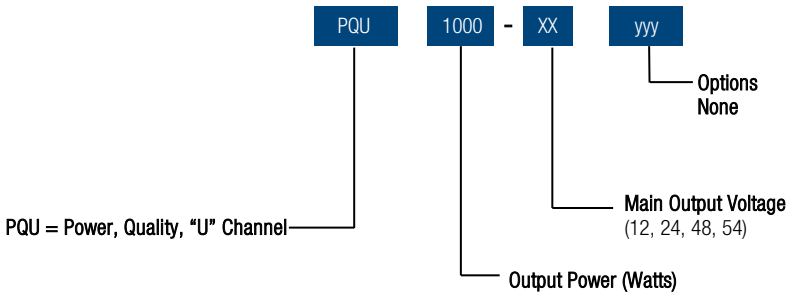
*VDD is an internal voltage rail derived from VSB and an internal housekeeping rail ("diode ORED") and is compatible with the voltage tolerances of VSB).
For robust PMBus communications, it is recommended SDA and SCA lines be pulled up via external resistors to a voltage of 3.3V or greater.

STATUS LEDS

Dual (Red and Green) LEDS:

PSU Status	LED Status
Output on and OK	Green
AC power not present	Off
Standby state; AC present; Main output off, VSB on	1Hz Blink Green
Power supply critical event causing a shutdown; failure, overcurrent, short circuit, overvoltage, fan failure, over temperature	Red
Power supply warning events where the power supply continues to operate; high temperature, high power, high current	1Hz Blink Red

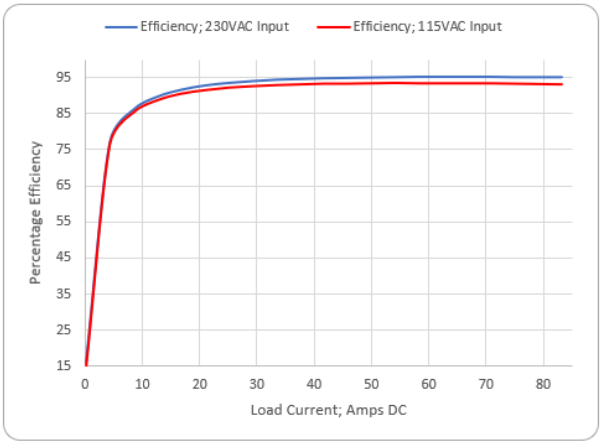
PART NUMBER STRUCTURE



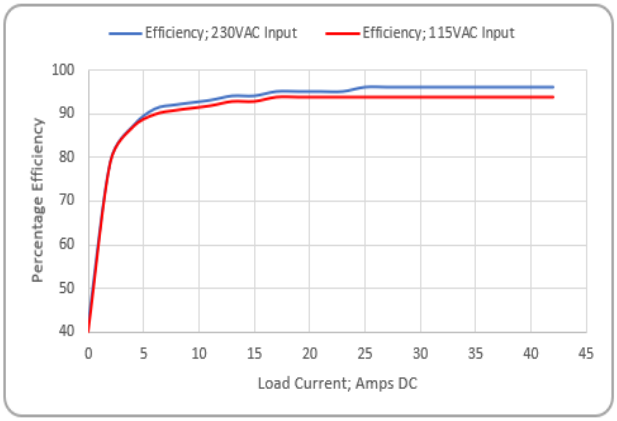
Examples: PQU1000-24 = Base 24V Model; no options

TYPICAL PERFORMANCE DATA EXAMPLES

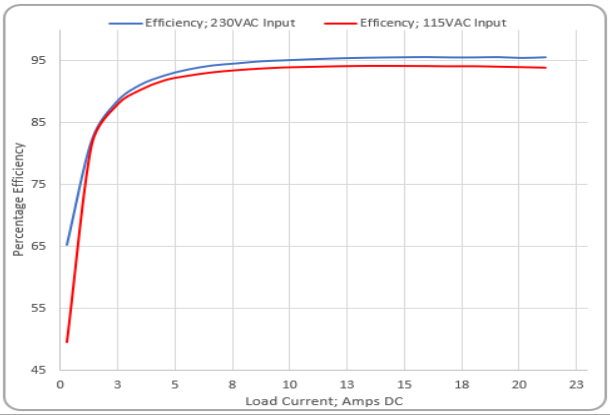
Efficiency PQU1000-12



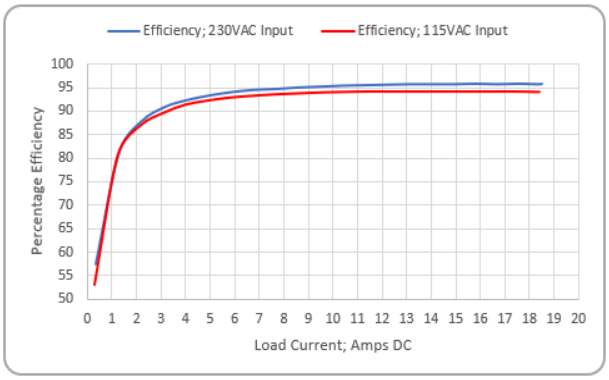
Efficiency PQU1000-24



Efficiency PQU1000-48

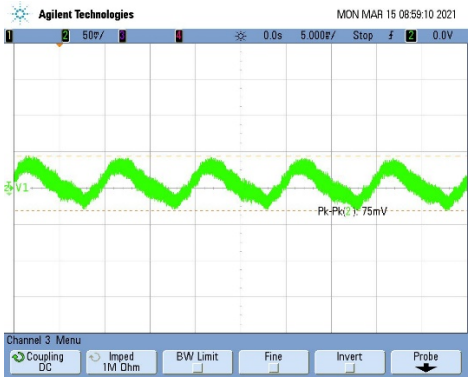


Efficiency PQU1000-54



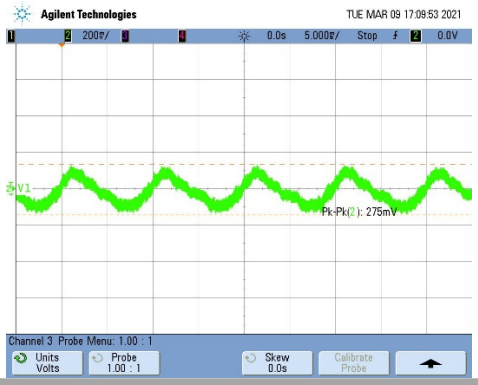
TYPICAL PERFORMANCE DATA EXAMPLES

Ripple & Noise PQU1000-12; Full Load; 264Vac Input



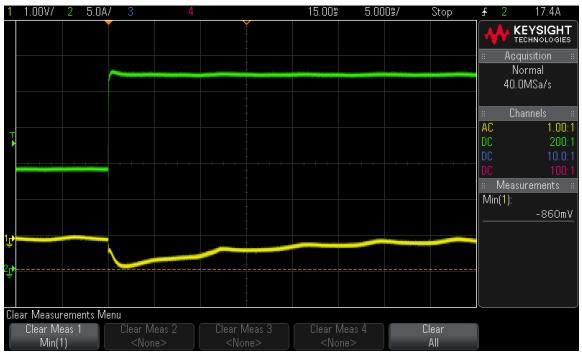
Ripple & Noise; PQU1000-24; Full Load: 264Vac Input

Ripple & Noise PQU1000-48; Full Load: 264Vac Input

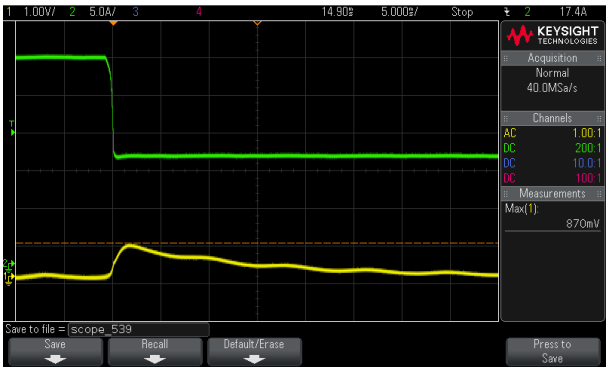


Ripple & Noise; PQU1000-54; Full Load: 264Vac Input

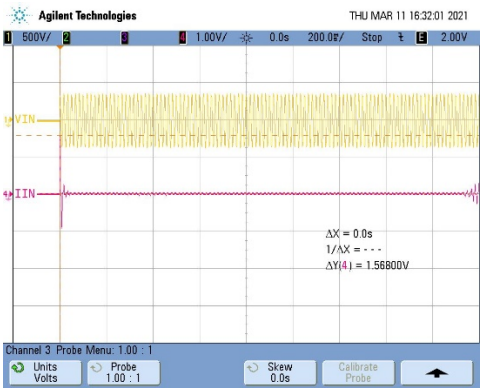
Transient Performance, 24V Model Half to Full Load



Transient Performance, 24V Model Full to Half Load



Inrush Current; 264Vac Input, Cold Start, Full Load; 31.4Apk

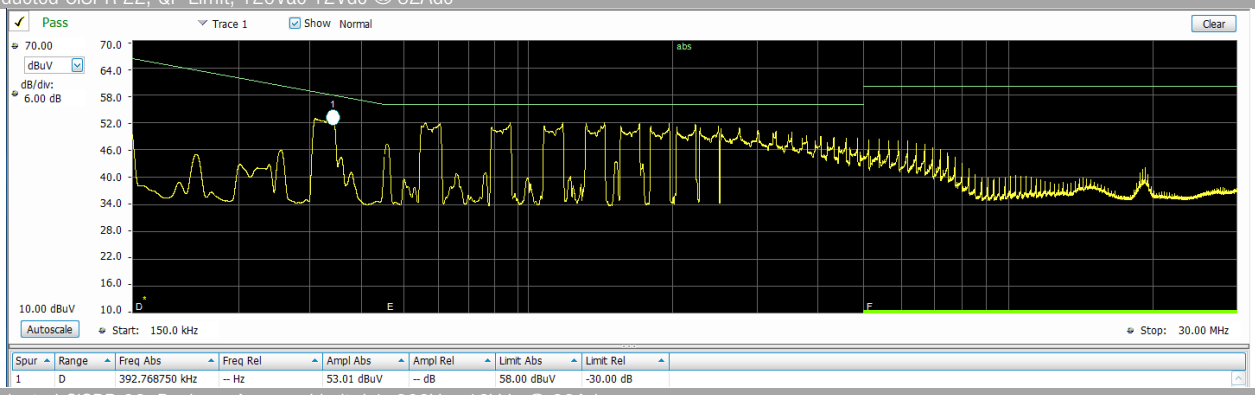


Hold Up Time; 90Vac; 1000W; 16.35ms

TYPICAL EMI PERFORMANCE DATA EXAMPLES

EMI; Conducted CISPR 32/EN55032 Class B; Peak vs. Average Limit; 120Vac 12Vdc @ 82Adc

EMI; Conducted CISPR 22; QP Limit; 120Vac 12Vdc @ 82Adc



EMI; Conducted CISPR 22; Peak vs. Average Limit; L1; 208Vac 12Vdc @ 82Adc

EMI; Conducted CISPR 22; QP Limit; 208Vac 12Vdc @ 82Adc

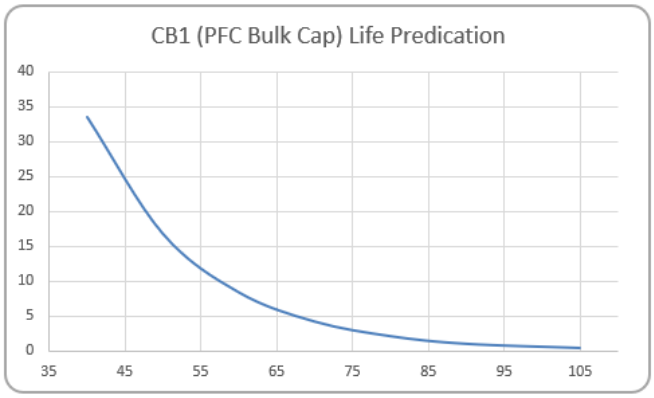
THERMAL CONSIDERATIONS

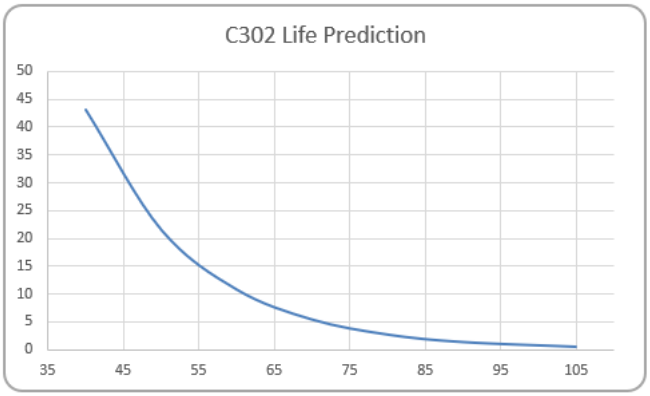
System thermal management is critical to the performance and reliability of the PQU1000 series power supplies.

The product is designed to provide 800W using natural convection cooling when mounted with un-obstructed convection current airflow flow at up to +50°C local ambient temperature. At elevated temperatures the power supply data is taken while it is surrounded by a large vented enclosure to minimize forced cross flows inherent in the elevated temperature test.
The product is capable of operation when mounted in diverse orientations; operational/derating curves shall be provided to show the effect of such mounting. See ACAN-TBA for additonal details

Capacitor Case Temperature and Mounting Orientation:
The power supply can operate in any orientation; however, the power supply contains overtemperature protection that will shut off the output as the temperature of critical componenets exceed their safe and reliable thermal limits.

The life expectancy of the power supply is inversely proportional to the case temperature of electrolytic capacitors. The designer of the system in which this power module is deployed should consider this relationship to ensure optium product life. The following charts are life predications (based on 80% of full load capability) that illustrate this relationship.



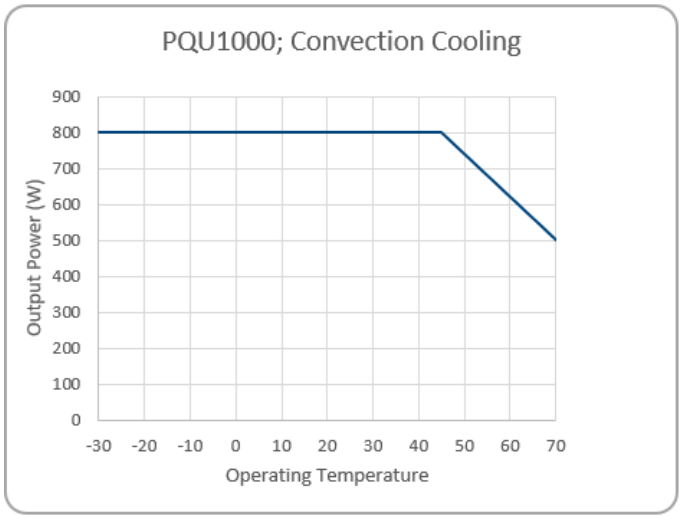


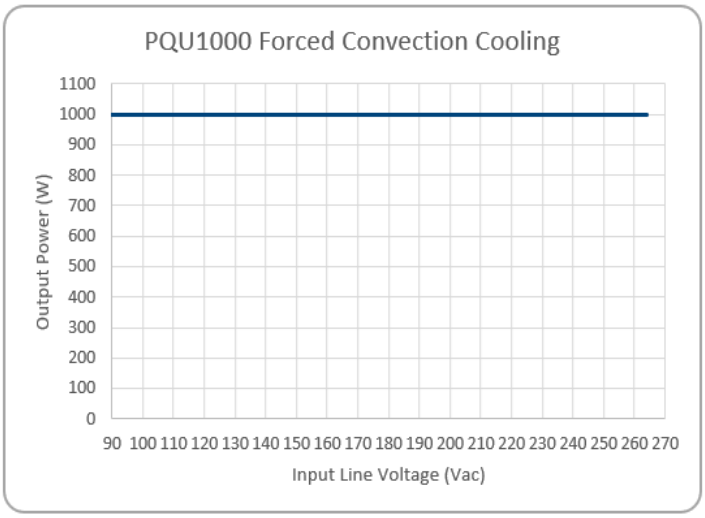
The PQU1000 Series will also benefit from the provision of forced convection cooling airflow either generated by an external host system fan or by a fan integral to the PQU1000-FT COVER assembly.
A dedicated 12V Fan (V3) output is provided that can be used to power an external (system) fan, or that of the PQU1000-FT-COVER.
This shall enable operation to the full capability of 1000W at +50°C local ambient (forced convection cooling air) temperature .
Please refer to ACAN-TBA for additonal details.

NB: The above curves are based on generic predicted life.

DERATNG CURVES

Thermal; Convection "U" Channel

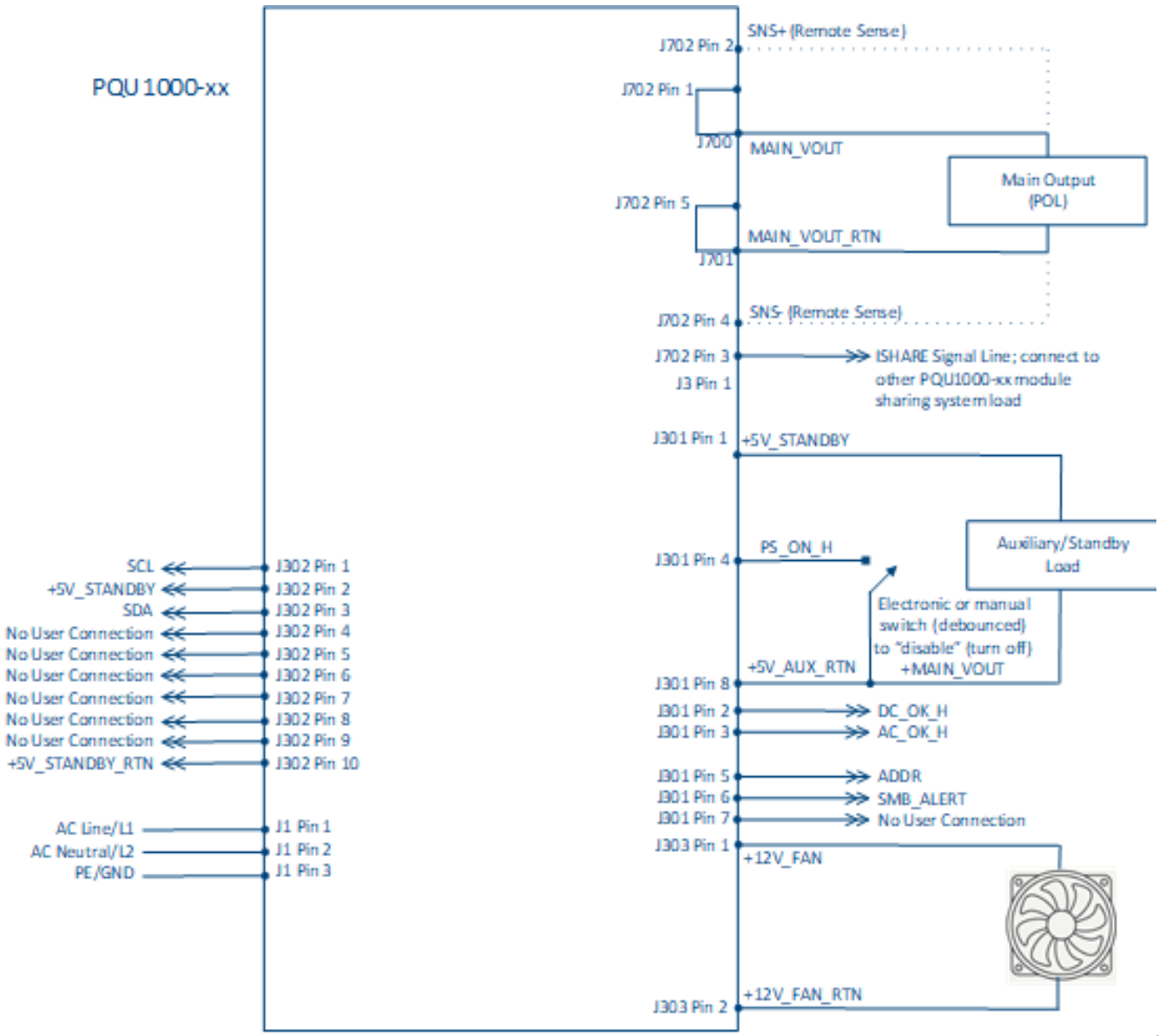




- The PQU1000 will reliably provide 750W cooled by natural convection at 90Vac input, at a local ambient temperature of +50C.
- Slight derating is applied with temperature from +45°C operation, linearly derating to 500W at +70°C;

No derating with input line voltage for convection or forced convection cooling airflow, for all series variants when cooled with top (cover) mounted fan

CONNECTION DIAGRAM

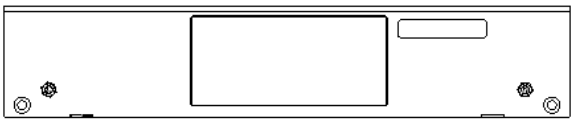


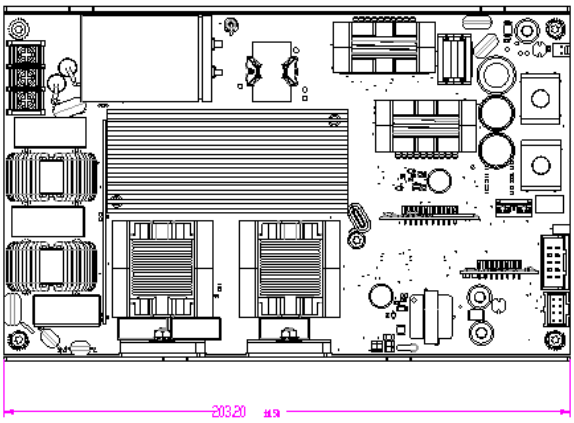
Note: For parallel (current share) operation it is required to connect the sharing power supplies in parallel (+DC out connected together and DC out Return connected together on sharing power supplies. Since each output has an identical "droop" share characteristic then each output will intrinsically share the total load current. See ACAN-TBA for more details.

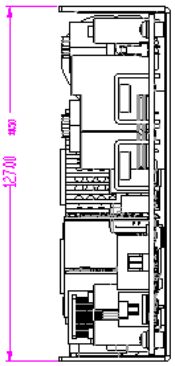
It is recommended that for redundant (critical) applications that external isolation devices (diodes or MOSFETS) are employed.

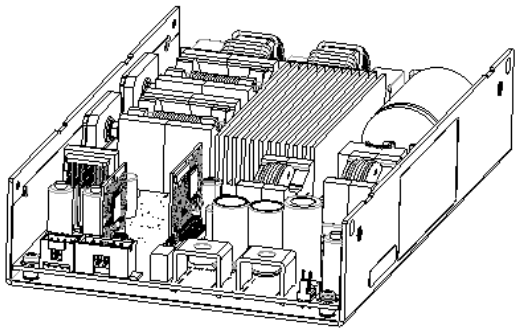
MECHANICAL DIMENSIONS (Nominal)

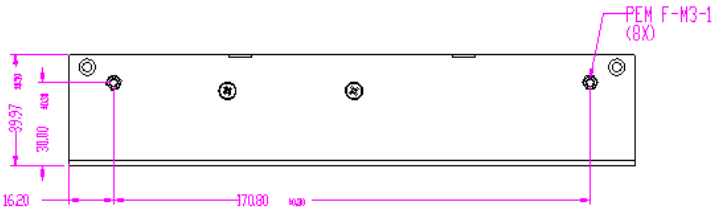
PQU1000-xx "U" CHANNEL DIMENSIONS











Input Connector J1	
Dinkle Part# DT-35-B01W-03 Supported Cable Gauge: 22-12AWG; 0.34-4.0mm^2 Torque screws not greater than 0.5Nm (4.4in lbs)	
Pin 1	AC Line/L1
Pin 2	AC Neutral/L2
Pin3	PE/GND

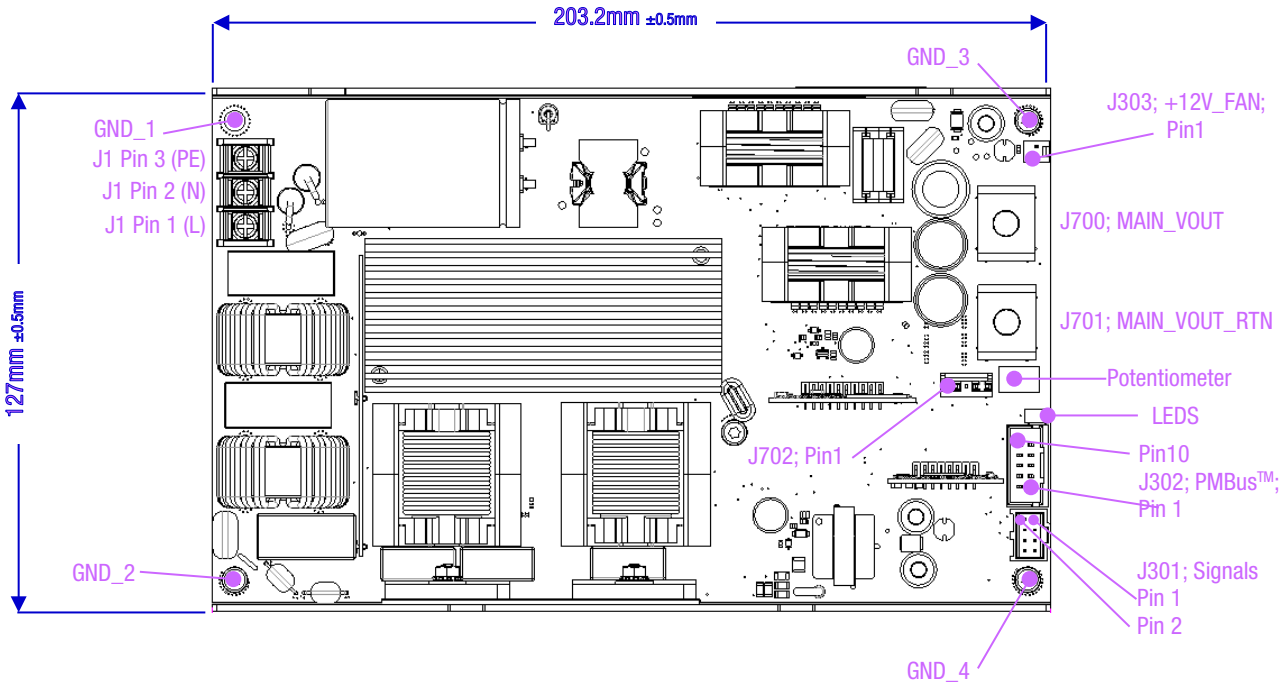
Output Connector J700, J701	
SCED Part#: AO-15/4J-N5 High Current Electrical Screwed Terminals Connectors 100A 10.0mm*16.5mm	
J700	MAIN_VOUT
J701	MAIN_VOUT_RTN

VSTANDBY & Signal Connector; J301	
PCB Connector: Leoco 2874P08VT0B02A000 Mating Connector	
Pin 1	+5V_STANDBY
Pin 2	PWR_OK
Pin 3	AC_OK
Pin 4	PS_ON_H
Pin 5	ADDR
Pin 6	SMB_ALERT
Pin 7	N/C
Pin 8	+5V_STANDBY_RTN

PMBus™ Connector; J302	
PCB Connector: • TE Connectivity; 5-102619-3 • Mating Connector; 3M 89110-010HA	
Pin 1	SCL
Pin 2	+5V_STANDBY_RTN
Pin 3	SDA
Pin 4	N/C
Pin 5	N/C
Pin 6	N/C
Pin 7	N/C
Pin 8	N/C
Pin 9	N/C
Pin 10	+5V_STANDBY_RTN

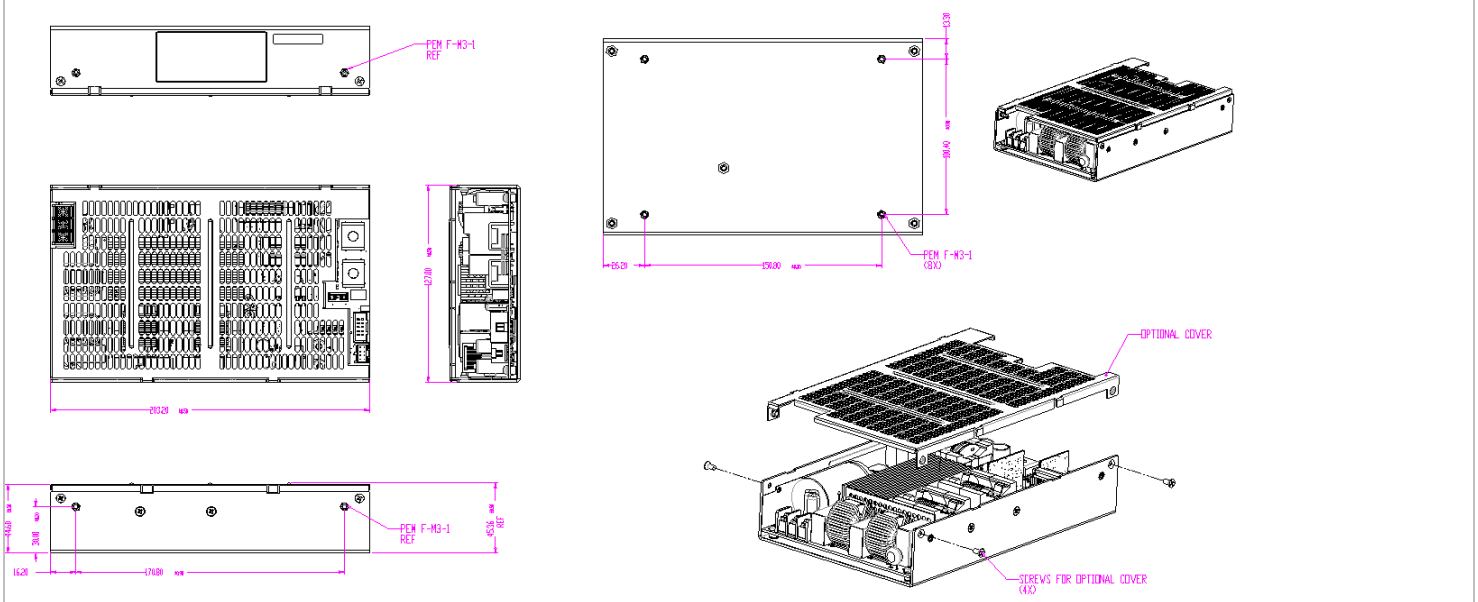
External Fan Connector; J303	
PCB Connector: • TE Connectivity; 640456-2 • Mating Connector: Molex 22-23-2021	
Pin 1	+12V_FAN
Pin 2	+12_FAN_RTN

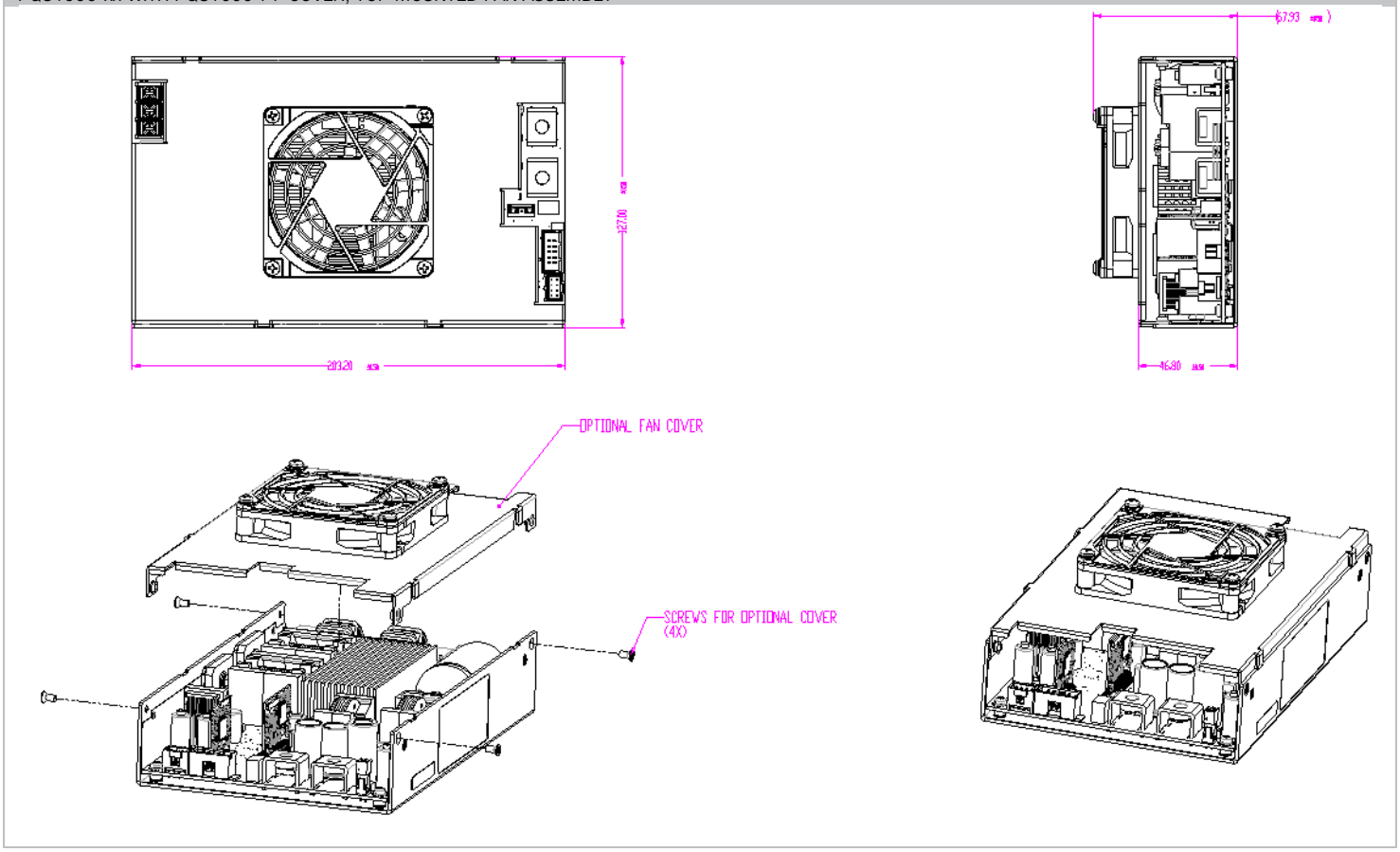
Remote Sense & ISHARE Connector; J702	
PCB Connector: • TE Connectivity; 640456-5 • Mating Connector: Molex 22-23-2051	
Pin 1	MAIN_VOUT
Pin 2	SNS+
Pin 3	ISHARE
Pin 4	SNS-
Pin 5	MAIN_VOUT_RTN



MECHANICAL DIMENSIONS (Nominal)

PQU1000-xx WITH PQU1000-COVER ASSEMBLY



MECHANICAL DIMENSIONS (Nominal)

PQU1000-xx WITH PQU1000-FT-COVER; TOP MOUNTED FAN ASSEMBLY