Nearstack 1.0+ Connector

PCIE® 5.0 AND LOW-PROFILE CONNECTOR

PCIe® 5.0, low-profile connector, and high density.

- Meets PCIe® 5.0 standards
- Low-profile connector
- Different pin count options
- Different shell tail length options
- Different cable exit options



TARGET MARKETS



FEATURES

- Meets PCIe® 5.0 standards
- Low-profile connector
- Small footprint
- Different shell tail length options
- Different cable exit options
- Different pin count options

BENEFITS

- Low-profile connector
- Small footprint

TECHNICAL INFORMATION

MATERIAL

- Housing: High-temperature thermoplastic, UL 94 V-0
- Contact: Copper Alloy and high-performance Copper Alloy options

MECHANICAL PERFORMANCE

- Mating Force: 2 N max. per diff pair (without latch)
- Unmating Force: 0.3 N min. per diff pair (without latch)
- Durability: 200 cycles

PACKAGING

- Tape & Reel

ELECTRICAL PERFORMANCE

- Current Rating:
  - Signal Contact: 0.65 Amps max. per mated contact pair without grouping restrictions with 30 AWG Wire
  - Signal Ended: 0.3 Amps max. per contact
- Low level contact resistance: ΔR 10 mΩ max. after test
- Voltage: 100 VDC

SPECIFICATIONS

- Amphenol Specification : PS-7811
- SFF-TA-1026

ENVIRONMENTAL

- Thermal Shock: No evidence of damage. EIA 364-32
- Humidity Temperature Cycling: No appearance damaged. EIA-364-31
- Temperature Life: No appearance damaged, EIA-364-31
- Salt Spray: Contact resistance ΔR 10 mΩ maximum. No exposure of the base metal due to corrosion. EIA 364-26

APPROVALS AND CERTIFICATION

- EIA

TARGET MARKETS/APPLICATIONS



Communications



Consumer



Data center
Server
Storage
Switch



Industrial & Instrumentation

PART NUMBERS

Description	Part Numbers
Nearstack connector	G95V1XXXXHR

Find part number details using the search box on www.amphenol-cs.com

WHERE INNOVATION CONNECTS
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CMIONSTK010725EA4

Disclaimer
Please note that the above information is subject to change without notice.